

INFORMATION DISCLOSURE CITATION

Docket Number

AD-303J

Application Number

10/090,941

Applicants

Stein et al.

Filing Date

March 5, 2002

Art Unit

2183

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	REF	DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	FILING DATE IF APPROPRIATE
WMT	1	6,587,864 B2	07/01/2003	Stein et al.	
	2	6,434,662 B1	08/13/2002	Greene et al.	
	3	6,349,318 B1	02/19/2002	Vanstone et al.	
	4	6,246,768 B1	06/12/2001	Kim	
	5	6,230,179 B1	05/08/2001	Dworkin et al.	
	6	6,199,087 B1	03/06/2001	Blake et al.	
	7	6,199,086 B1	03/06/2001	Dworkin et al.	
	8	6,049,815	04/11/2000	Lambert et al.	
	9	5,689,452	11/18/1997	Cameron	
	10	5,446,850	08/29/1995	Jeremiah et al.	
WMT	11	5,379,243	01/03/1995	Greenberger et al.	

FOREIGN PATENT DOCUMENTS

	REF	FOREIGN DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	Translation	
					YES	NO
WMT	12	EP 1 246 389 A1	10/02/2002	Europe		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

WMT	13	Viktor Fischer, <i>Realization of the Round 2 AES Candidates Using Altera FPGA</i> , (January 26, 2001) < http://csrc.nist.gov/CryptoToolkit/aes/roun2/conf3/papers/24-vfischer.pdf > (Micronic - Kosice, Slovakia).
WMT	14	Máire McLoone and J.V. McCanny, <i>High Performance Single-Chip FPGA Rijndael Algorithm Implementations</i> , CHES 2001 PROC, LNCS 2162, 65-76 (Ç.K. Koç et al. eds. 16 May 2001).

EXAMINER

W. TREAT

DATE CONSIDERED

12/2/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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WMT	15	5,214,763	05/25/1993	Blaner et al.	
OIP	16	5,095,525	03/10/1992	Almgren et al.	
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	18	4,852,098	07/25/1989	Brechard et al.	
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	20	4,722,050	01/26/1988	Lee et al.	
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	22	US 2003/0149857 A1	08/07/2003	Stein et al.	
	23	US 2003/0140213 A1	07/24/2003	Stein et al.	
	24	US 2003/0140211 A1	07/24/2003	Stein et al.	
WMT	25	US 2003/0133568 A1	07/17/2003	Stein et al.	

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WMT	26	elixent, <i>Changing the Electronic Landscape</i> (2001) < http://www.elixent.com > (elixent - Bristol, UK).
WMT	27	elixent Application Note <i>JPEG Codec</i> (Dec. 9, 2002) < http://www.elixent.com/assets/jpeg-coder.pdf > (elixent - Bristol, UK).

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	29	US 2003/0110196 A1	06/12/2003	Stein et al.	
	30	US 2003/0105791 A1	06/05/2003	Stein et al.	
	31	US 2003/0103626 A1	06/05/2003	Stein et al.	
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	33	US 2002/0041685 A1	04/11/2002	McLoone et al.	
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WAT	36	V. Baumgarte et al., <i>PACT XPP - A Self-Reconfigurable Data Processing Architecture</i> (June 2001) < http://www.pactcorp.com/xnew/download/ersa01.pdf > (PACT XPP - Santa Clara, CA).
WAT	37	PACT Informationstechnologie GmbH, <i>The XPP White Paper Release 2.1</i> (March 27, 2002) < http://www.pactcorp.com/xnew/download/xpp_white_paper.pdf > (PACT XPP - Santa Clara, CA).

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